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TRACING CONTROL SYSTEM FOR TASK

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ABSTRACT

PURPOSE: To trace a designated task only without giving an influence to other task by setting the debugging task, executing the embedding operation of an inequality instruction, stopping and restarting the target task by the inequality instruction interruption, and executing the trace processing.

CONSTITUTION: A debugger task 2-3 is set, and the program prepared by the secondary operating system 2-2 is loaded as an on-line task. Next, the break command name, the target task number and the head address to execute the tracing are inputted from a console 2-4. When the task 2-3 receives the break command, the command is analyzed, the inequality instruction is set to the designating address and the task 2-3 has the inequality instruction interruption. At such a time, the contents of the address embedding the designating task number and inequality instruction and the address before embedding the inequality instruction are stored at the register. When the inequality instruction interruption occurs, the break stop is executed, the task 2-3 waits for the command, the command to execute the tracing is inputted from the console 2-4 and the *tracing* processing is executed.